

In the Claims

1. (Original) An organic EL display panel comprising:
an insulating substrate;
a polysilicon layer formed on the substrate;
a gate insulating layer formed on the polysilicon layer;
a gate wire formed on the gate insulating layer;
an interlayer insulating film formed on the gate wire;
a data wire formed on the interlayer insulating film;
a pixel electrode formed on the same layer as the data wire;
an organic EL layer formed on the pixel electrode and disposed in a predetermined area;
a partition formed on the data wire and the pixel electrode and defining the predetermined area; and
a common electrode formed on the organic EL layer and the partition.
2. (Original) The organic EL display panel of claim 1, wherein the pixel electrode includes the same material as the data wire.
3. (Previously Presented) The organic EL display panel of claim 1, wherein the polysilicon layer comprises first and second transistor portions including source regions and drain regions and a storage electrode portion connected to the second transistor portion, the gate wire comprises first and second gate electrodes and a storage electrode overlapping the first and the second transistor portions and the storage electrode portion, respectively, the data wire comprises first and second data lines, a first source electrode connected to the first data line and the source region of the first transistor portion, a first drain electrode connected to the drain region of the first transistor portion and the second gate electrode, and a second source electrode connected to the second data line and the source region of the second transistor portion, and the pixel electrode is connected to the drain region of the second transistor.
4. (Previously Presented) The organic EL display panel of claim 1, further comprising a buffer layer disposed between the organic EL layer and the common electrode.

5. (Previously Presented) The organic EL display panel of claim 1, wherein the partition comprises black photoresist.

6. (Previously Presented) The organic EL display panel of claim 1, further comprising an auxiliary electrode contacting the common electrode.

7-9. (Canceled)

10. (Previously Presented) The organic EL display panel of claim 2, wherein the polysilicon layer comprises first and second transistor portions including source regions and drain regions and a storage electrode portion connected to the second transistor portion, the gate wire comprises first and second gate electrodes and a storage electrode overlapping the first and the second transistor portions and the storage electrode portion, respectively, the data wire comprises first and second data lines, a first source electrode connected to the first data line and the source region of the first transistor portion, a first drain electrode connected to the drain region of the first transistor portion and the second gate electrode, and a second source electrode connected to the second data line and the source region of the second transistor portion, and the pixel electrode is connected to the drain region of the second transistor.

11. (Previously Presented) The organic EL display panel of claim 2, further comprising a buffer layer disposed between the organic EL layer and the common electrode.

12. (Previously Presented) The organic EL display panel of claim 2, wherein the partition comprises black photoresist.

13. (Previously Presented) The organic EL display panel of claim 2, further comprising an auxiliary electrode contacting the common electrode.

14. (Canceled)

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